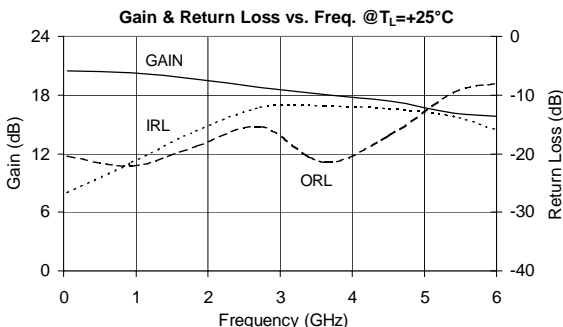




## Product Description

Sirenza Microdevices' NGA-589 is a high performance InGaP/GaAs HBT MMIC Amplifier. A Darlington configuration designed with InGaP process technology provides broadband performance up to 5.5 GHz with excellent thermal performance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. Cancellation of emitter junction non-linearities results in higher suppression of intermodulation products. At 850 MHz and 80mA, the NGA-589 typically provides +39 dBm output IP<sub>3</sub>, 20 dB of gain, and +19 dBm of 1dB compressed power using a single positive voltage supply. Only 2 DC-blocking capacitors, a bias resistor and an optional RF choke are required for operation.



## NGA-589

### DC-5.5 GHz, Cascadable InGaP/GaAs HBT MMIC Amplifier



### Product Features

- High Gain : 19.2 dB at 1950 MHz
- Cascadable 50 Ohm
- Patented InGaP Technology
- Operates From Single Supply
- Low Thermal Resistance Package

### Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

Symbol	Parameter	Units	Frequency	Min.	Typ.	Max.
G	Small Signal Gain	dB	850 MHz 1950 MHz 2400 MHz	18.0	20.0 19.2 18.9	22.0
P <sub>1dB</sub>	Output Power at 1dB Compression	dBm	850 MHz 1950 MHz		19.0 18.8	
OIP <sub>3</sub>	Output Third Order Intercept Point	dBm	850 MHz 1950 MHz		39.0 34.0	
Bandwidth	Determined by Return Loss (>10dB)	MHz			5500	
IRL	Input Return Loss	dB	1950 MHz		15.5	
ORL	Output Return Loss	dB	1950 MHz		18.0	
NF	Noise Figure	dB	1950 MHz		3.7	
V <sub>D</sub>	Device Operating Voltage	V		4.5	4.9	5.3
I <sub>D</sub>	Device Operating Current	mA		72	80	88
R <sub>TH</sub> , J-I	Thermal Resistance (junction to lead)	°C/W			111	

**Test Conditions:** V<sub>S</sub> = 8 V, I<sub>D</sub> = 80 mA Typ., OIP<sub>3</sub> Tone Spacing = 1 MHz, Pout per tone = 0 dBm  
R<sub>BIAS</sub> = 39 Ohms, T<sub>L</sub> = 25°C, Z<sub>S</sub> = Z<sub>L</sub> = 50 Ohms

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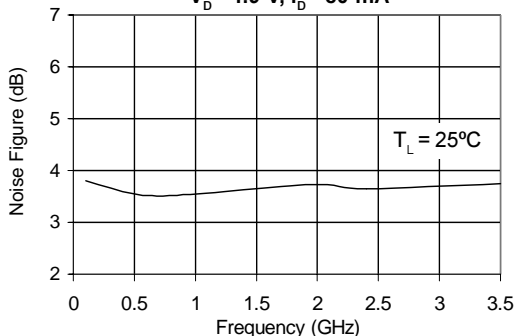
## Typical RF Performance at Key Operating Frequencies

Symbol	Parameter	Unit	Frequency (MHz)					
			100	500	850	1950	2400	3500
G	Small Signal Gain	dB	20.5	20.3	20.0	19.2	19.0	18.4
OIP <sub>3</sub>	Output Third Order Intercept Point	dBm	37.7	38.8	39.0	34.0	31.4	27.7
P <sub>1dB</sub>	Output Power at 1dB Compression	dBm	18.3	19.0	19.0	18.8	17.6	15.4
IRL	Input Return Loss	dB	27.0	24.5	21.8	15.5	13.2	11.7
ORL	Output Return Loss	dB	20.9	21.4	22.1	18.0	15.9	19.3
S <sub>12</sub>	Reverse Isolation	dB	22.6	22.7	22.8	23.2	23.4	24.0
NF	Noise Figure	dB	3.8	3.6	3.5	3.7	3.6	3.8

**Test Conditions:**  $V_S = 8\text{ V}$ ,  $I_D = 80\text{ mA}$  Typ., OIP<sub>3</sub> Tone Spacing = 1 MHz, Pout per tone = 0 dBm  
 $R_{BIAS} = 39\text{ Ohms}$ ,  $T_L = 25^\circ\text{C}$ ,  $Z_S = Z_L = 50\text{ Ohms}$

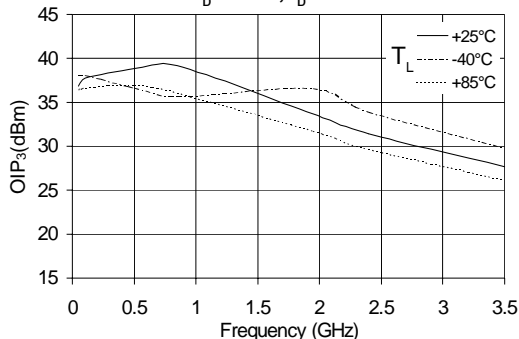
### Noise Figure vs. Frequency

$V_D = 4.9\text{ V}$ ,  $I_D = 80\text{ mA}$



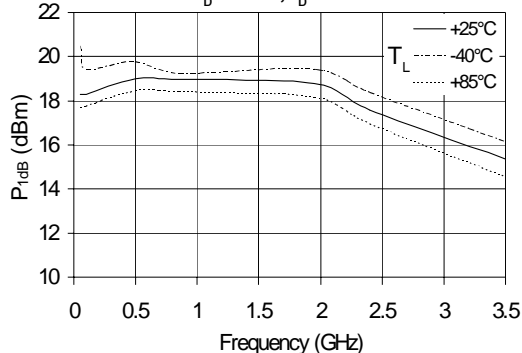
### OIP<sub>3</sub> vs. Frequency

$V_D = 4.9\text{ V}$ ,  $I_D = 80\text{ mA}$



### P<sub>1dB</sub> vs. Frequency

$V_D = 4.9\text{ V}$ ,  $I_D = 80\text{ mA}$

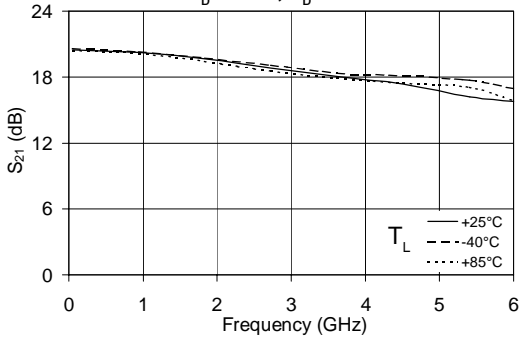


### Absolute Maximum Ratings

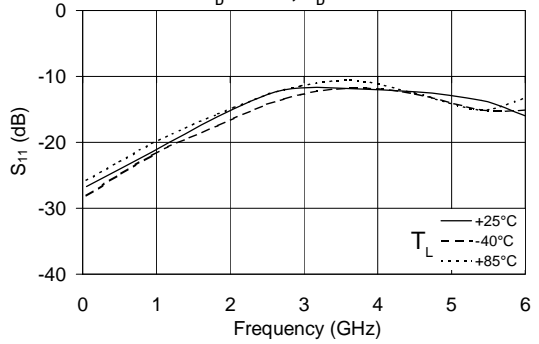
Parameter	Absolute Limit
Max. Device Current ( $I_D$ )	120 mA
Max. Device Voltage ( $V_D$ )	6 V
Max. RF Input Power	+15 dBm
Max. Junction Temp. ( $T_J$ )	+150°C
Operating Temp. Range ( $T_L$ )	-40°C to +85°C
Max. Storage Temp.	+150°C

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.  
 Bias Conditions should also satisfy the following expression:  
 $I_D V_D < (T_J - T_L) / R_{TH} j-I$

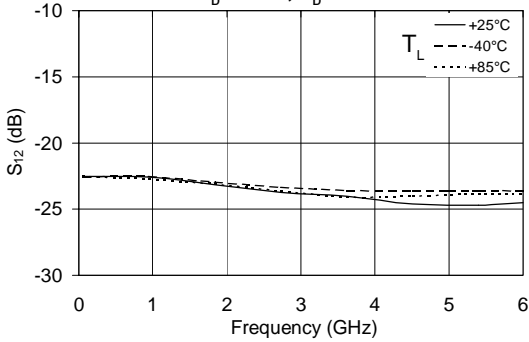
**$|S_{21}|$  vs. Frequency**  
 $V_D = 4.9$  V,  $I_D = 80$  mA



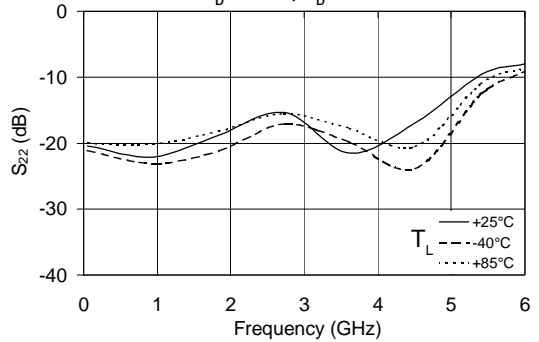
**$|S_{11}|$  vs. Frequency**  
 $V_D = 4.9$  V,  $I_D = 80$  mA



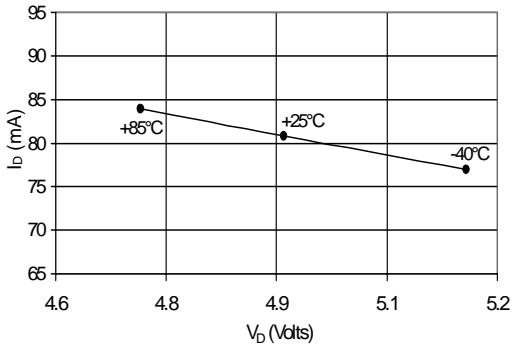
**$|S_{12}|$  vs. Frequency**  
 $V_D = 4.9$  V,  $I_D = 80$  mA



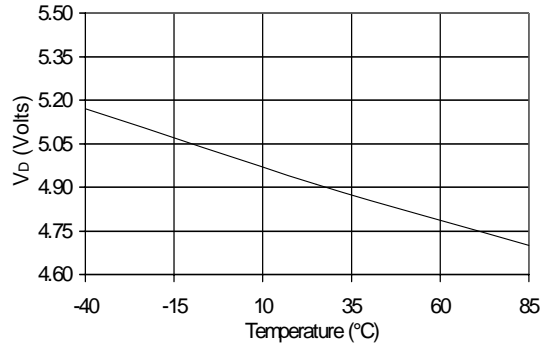
**$|S_{22}|$  vs. Frequency**  
 $V_D = 4.9$  V,  $I_D = 80$  mA



**$V_D$  vs.  $I_D$  over Temperature for fixed**  
 $V_S = 8$  V,  $R_{BIAS} = 39$  Ohms \*

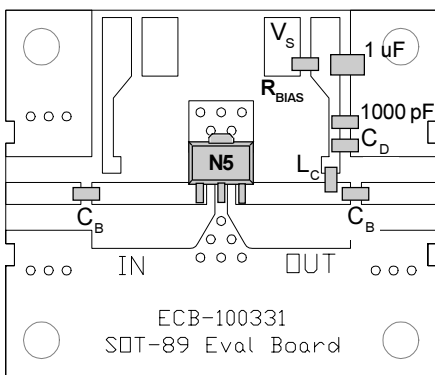
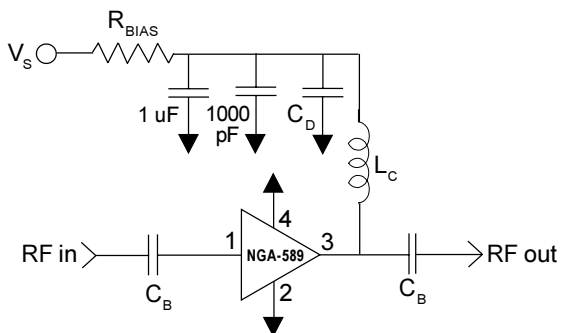


**$V_D$  vs. Temperature for Constant  $I_D = 80$  mA**



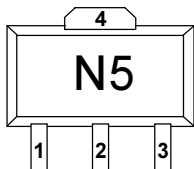
\* Note: In the applications circuit on page 4,  $R_{BIAS}$  compensates for voltage and current variation over temperature.

## Basic Application Circuit



### Part Identification Marking

The part will be marked with an "N5" designator on the top surface of the package.



### Caution: ESD sensitive

Appropriate precautions in handling, packaging and testing devices must be observed.

### Application Circuit Element Values

Reference Designator	Frequency (Mhz)				
	500	850	1950	2400	3500
C <sub>B</sub>	220 pF	100 pF	68 pF	56 pF	39 pF
C <sub>D</sub>	100 pF	68 pF	22 pF	22 pF	15 pF
L <sub>C</sub>	68 nH	33 nH	22 nH	18 nH	15 nH

### Recommended Bias Resistor Values for I<sub>D</sub>=80mA

Supply Voltage(V <sub>S</sub> )	7.5 V	8 V	10 V	12 V
R <sub>BIAS</sub>	33 Ω	39 Ω	62 Ω	91 Ω

Note: R<sub>BIAS</sub> provides DC bias stability over temperature.

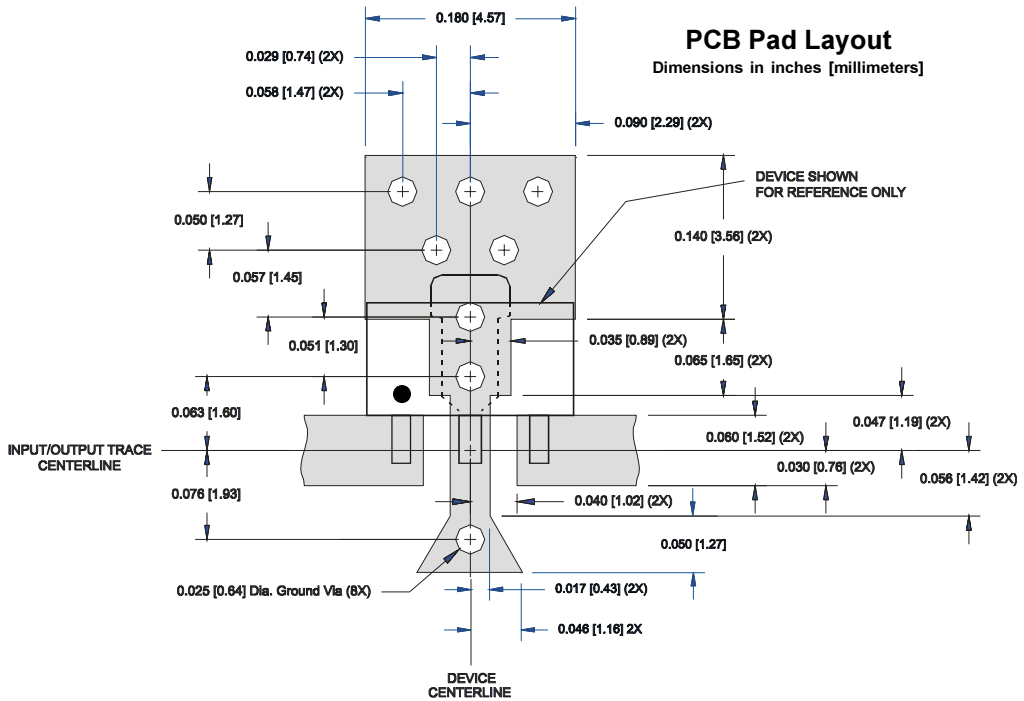
### Mounting Instructions

1. Solder the copper pad on the backside of the device package to the ground plane.
2. Use a large ground pad area with many plated through-holes as shown.
3. We recommend 1 or 2 ounce copper. Measurement for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.

Pin #	Function	Description
1	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
2, 4	GND	Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible.
3	RF OUT/BIAS	RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.

### Part Number Ordering Information

Part Number	Reel Size	Devices/Reel
NGA-589	7"	1000



### Nominal Package Dimensions

Dimensions in inches [millimeters]

Refer to package drawing posted at [www.sirenza.com](http://www.sirenza.com) for tolerances.

